





Docket No.: SUND-501CIP

(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Chen-Chih Huang et al.

Application No.: 10/773,450

Filed: December 2, 2004

For: Phase interpolation circuit Confirmation No.: 3611

Art Unit: 2816

Examiner: L. M. Nguyen

INFORMATION DISCLOSURE STATEMENT (IDS)

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 CFR 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO/SB/08. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is filed before the mailing date of a first Office Action on the merits as far as is known to the undersigned (37 CFR 1.97(b)(3)).

In accordance with 37 CFR 1.98(a)(2)(ii), Applicant has not submitted copies of U.S. patents and U.S. patent applications. Applicant submits herewith a copy of a nonpatent literature in accordance with 37 CFR 1.98(a)(2).

Dated: November 30, 2006

Respectfully submitted,

Steven M. Rabin

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Correspondence Customer Number: 23995

PTO/SB/08A/B (09-06)
Approved for use through 03/31/2007. OMB 0651-0031
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

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Substitute for form 1449/PTO				Complete if Known		
				Application Number	10/773,450-Conf. #3611	
l IN	IFORMATIO	N DI	SCLOSURE	Filing Date	December 2, 2004	
S	TATEMENT	BY A	APPLICANT	First Named Inventor	Chen-Chih Huang	
				Art Unit	2816	
	(Use as many s	heets as	s necessary)	Examiner Name	L. M. Nguyen	
Sheet	1	of	1	Attorney Docket Number	SUND-501CIP	

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No.1	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No.1	Foreign Patent Document Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
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	NON PATENT LITERATURE DOCUMENTS					
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	CA	HERVÉ MARIE AND PHILIPPE BELIN, R, G, B Acquisition Interface with Line-Locked Clock Generator for Flat Panel Display, IEEE JOURNAL OF SOLID-STATE CIRCUITS, JULY 1998, pages 1009-1013, VOL. 33, NO. 7, PHILIPS SEMICONDUCTORS, CAEN, FRANCE				

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